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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: M. TADOKORO, et al.  
Application No: 09/893,577  
Filed: June 29, 2001  
Title: FABRICATION METHOD OF SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE  
Art Unit: 1765  
Examiner: Kin Chan CHEN

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

July 19, 2004

Sir:

In response to the Office Action mailed April 19, 2004, please amend the above-identified application as listed in the following, and as set forth on the following pages:

**Amendments to the Claims; and**

**Remarks are included following the amendments.**